

FIGURE 1A

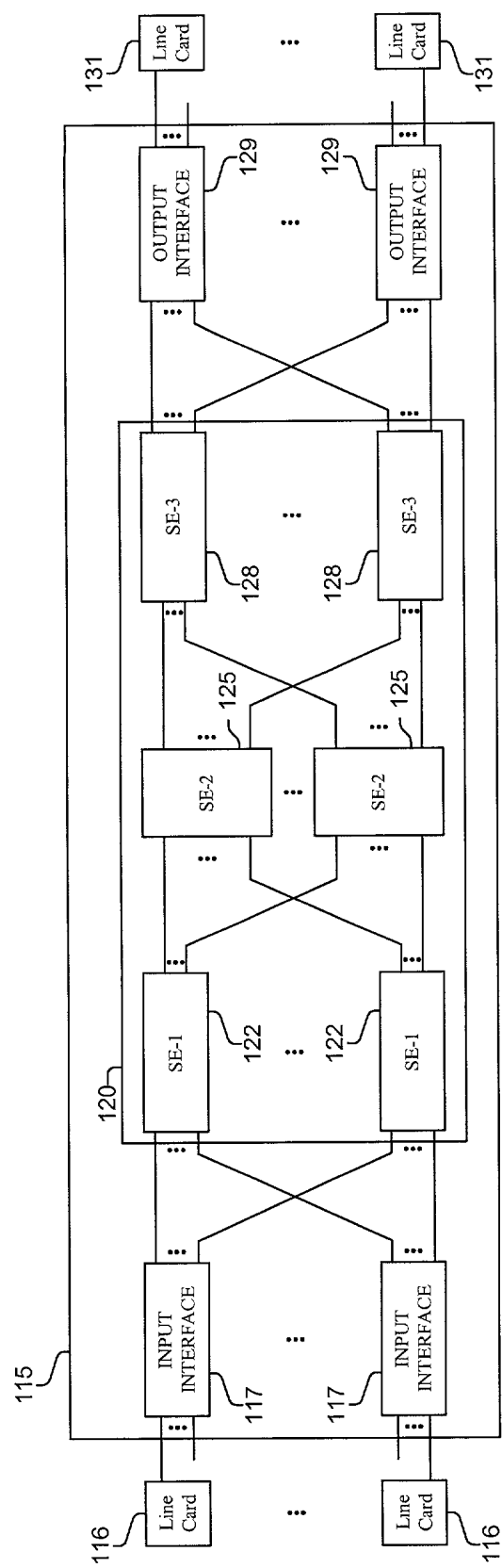


FIGURE 1B

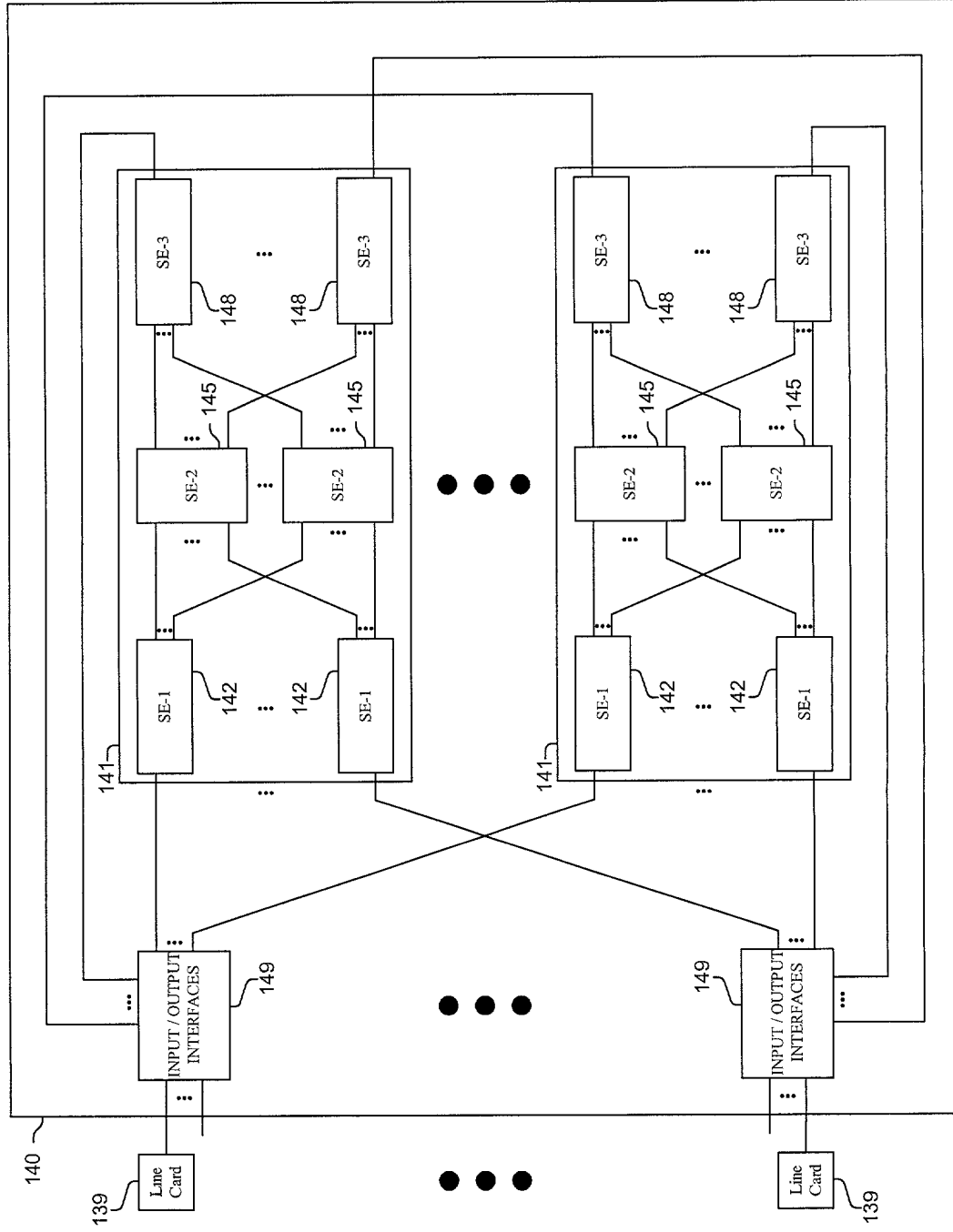


FIGURE 1C

FIG. 1D is a block diagram of a system 160, which may be a network switch or a router, according to one embodiment. The system 160 includes a plurality of line cards 158, each of which is connected to a corresponding input/output interface 169. The input/output interfaces 169 are connected to a central switching fabric 161, which is connected to a plurality of output interfaces 164. The switching fabric 161 is configured to route data packets received from the line cards 158 to the output interfaces 164. The system 160 may be implemented using a variety of hardware and software components, and may be used in a variety of network environments.

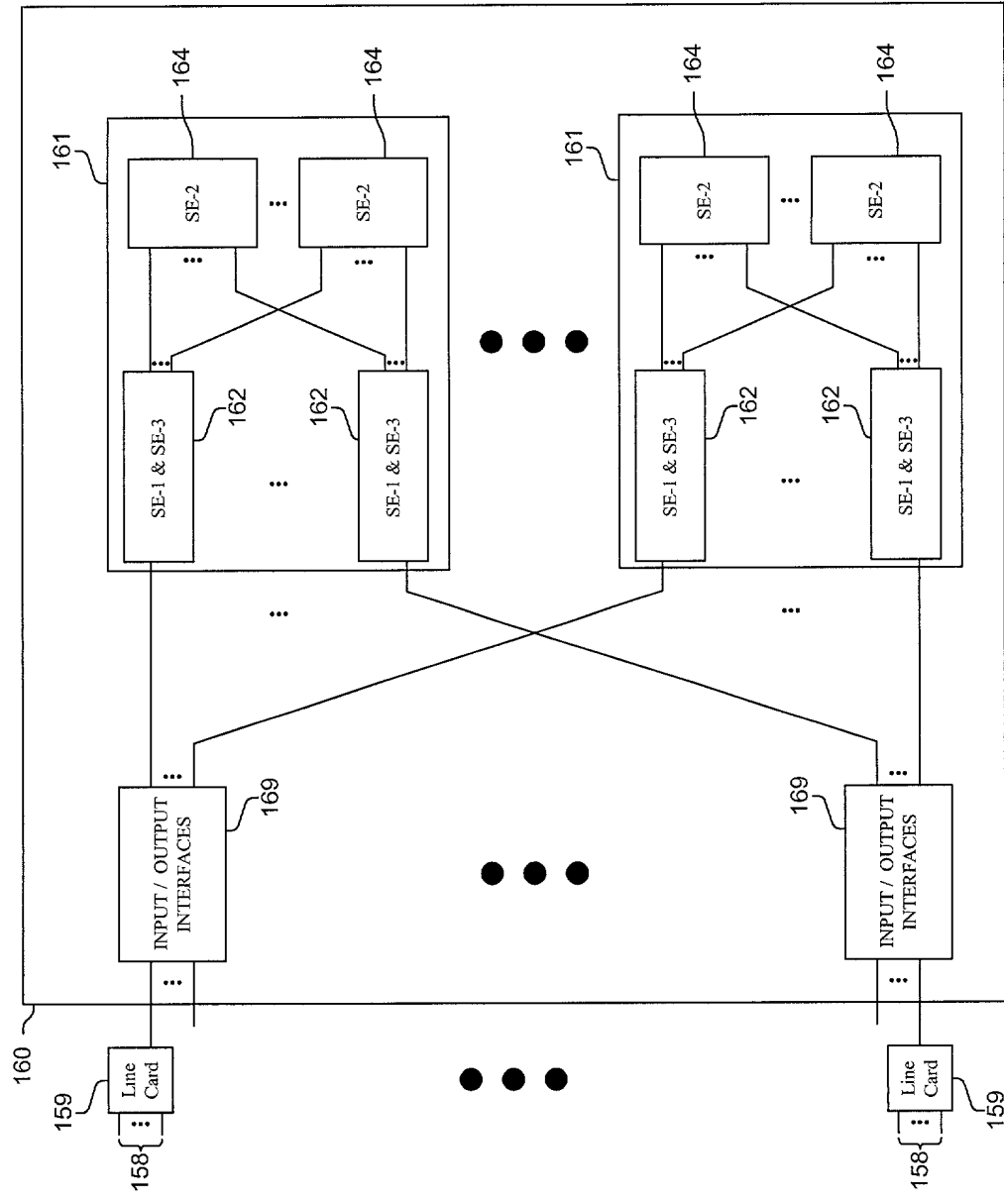


FIGURE 1D

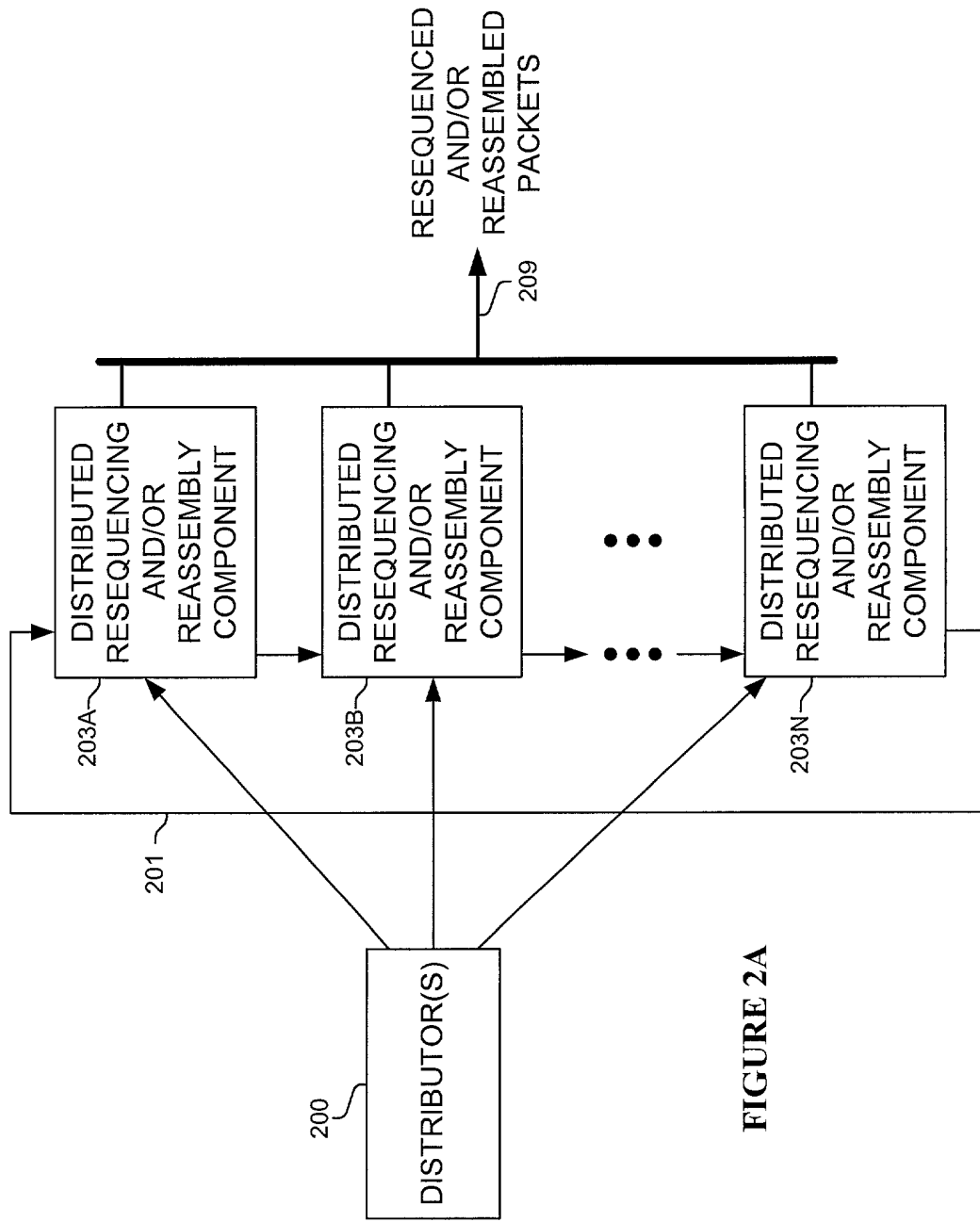


FIGURE 2A

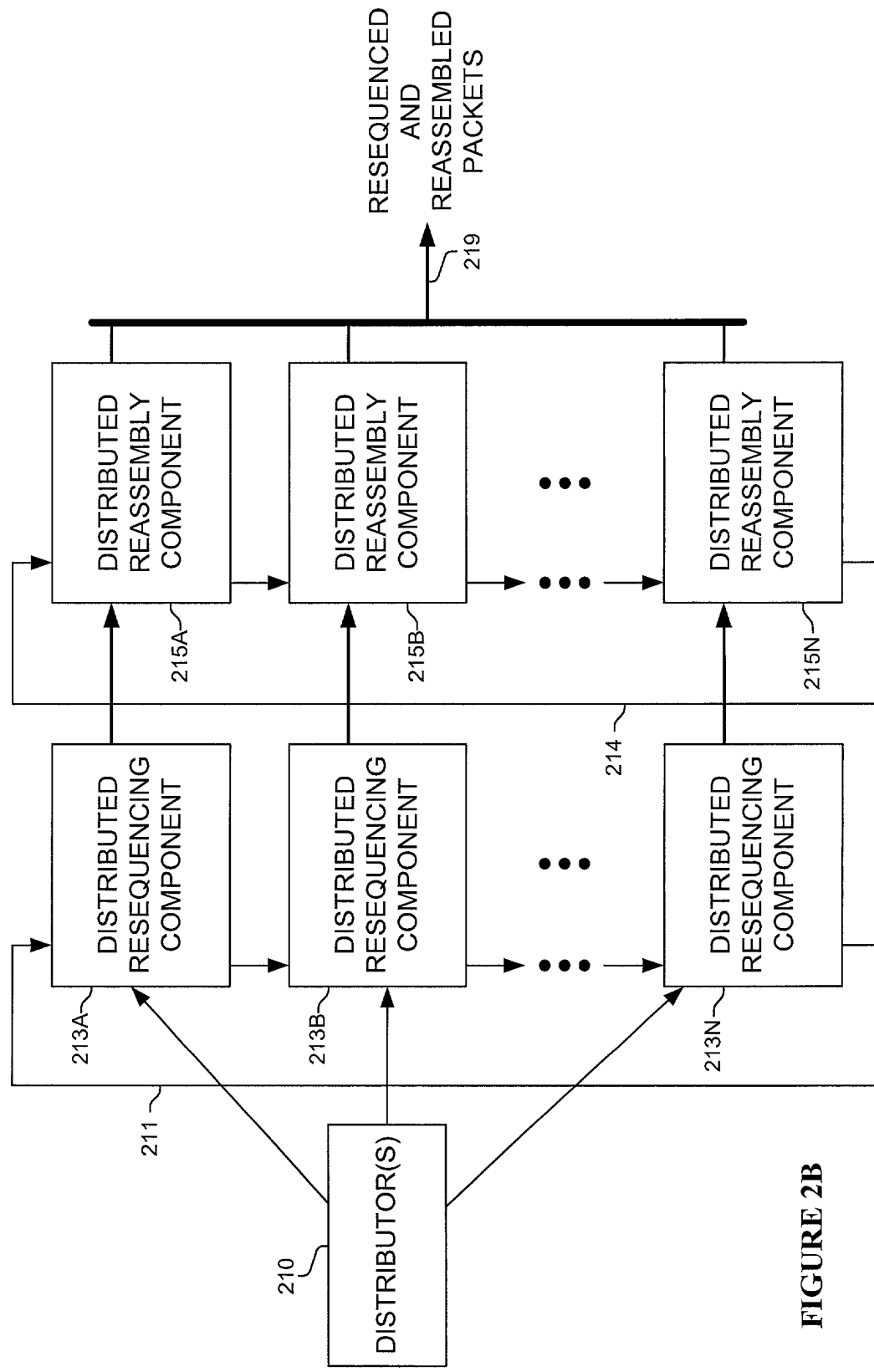


FIGURE 2B

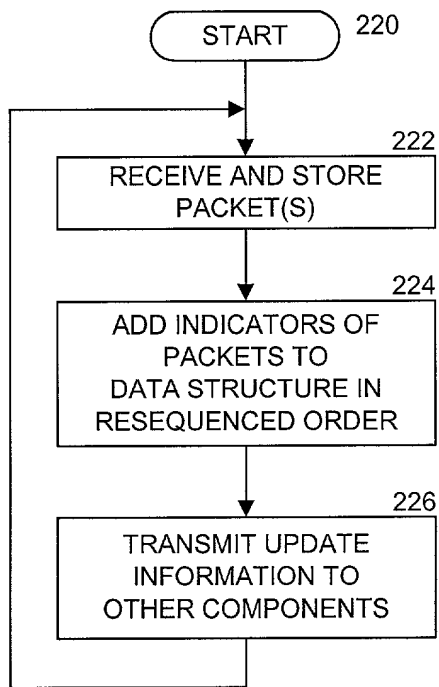


FIGURE 2C

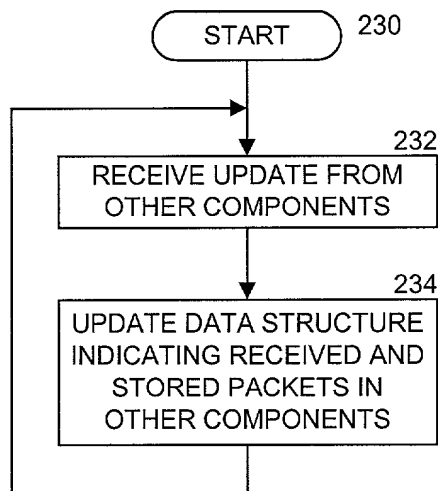


FIGURE 2D

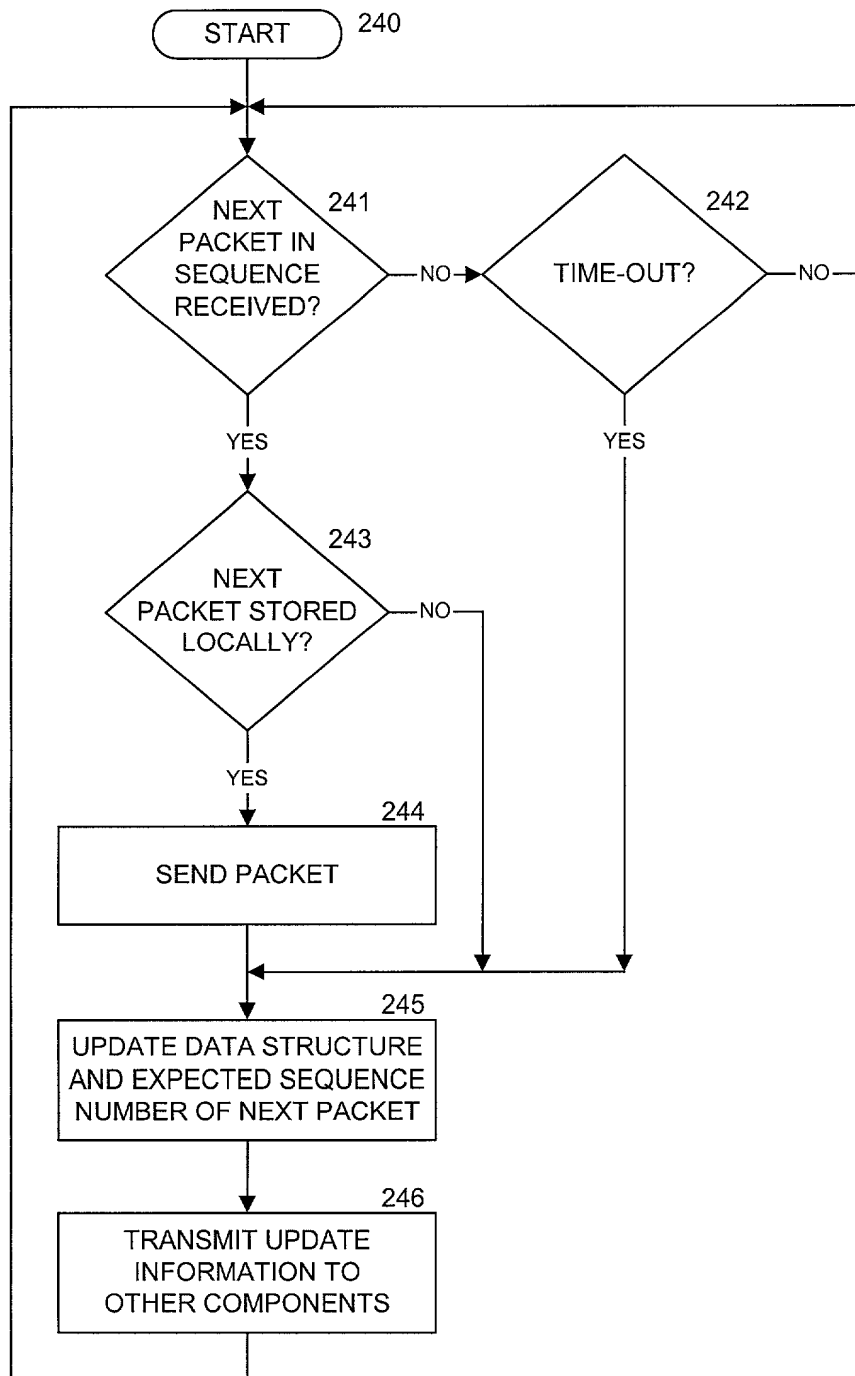


FIGURE 2E

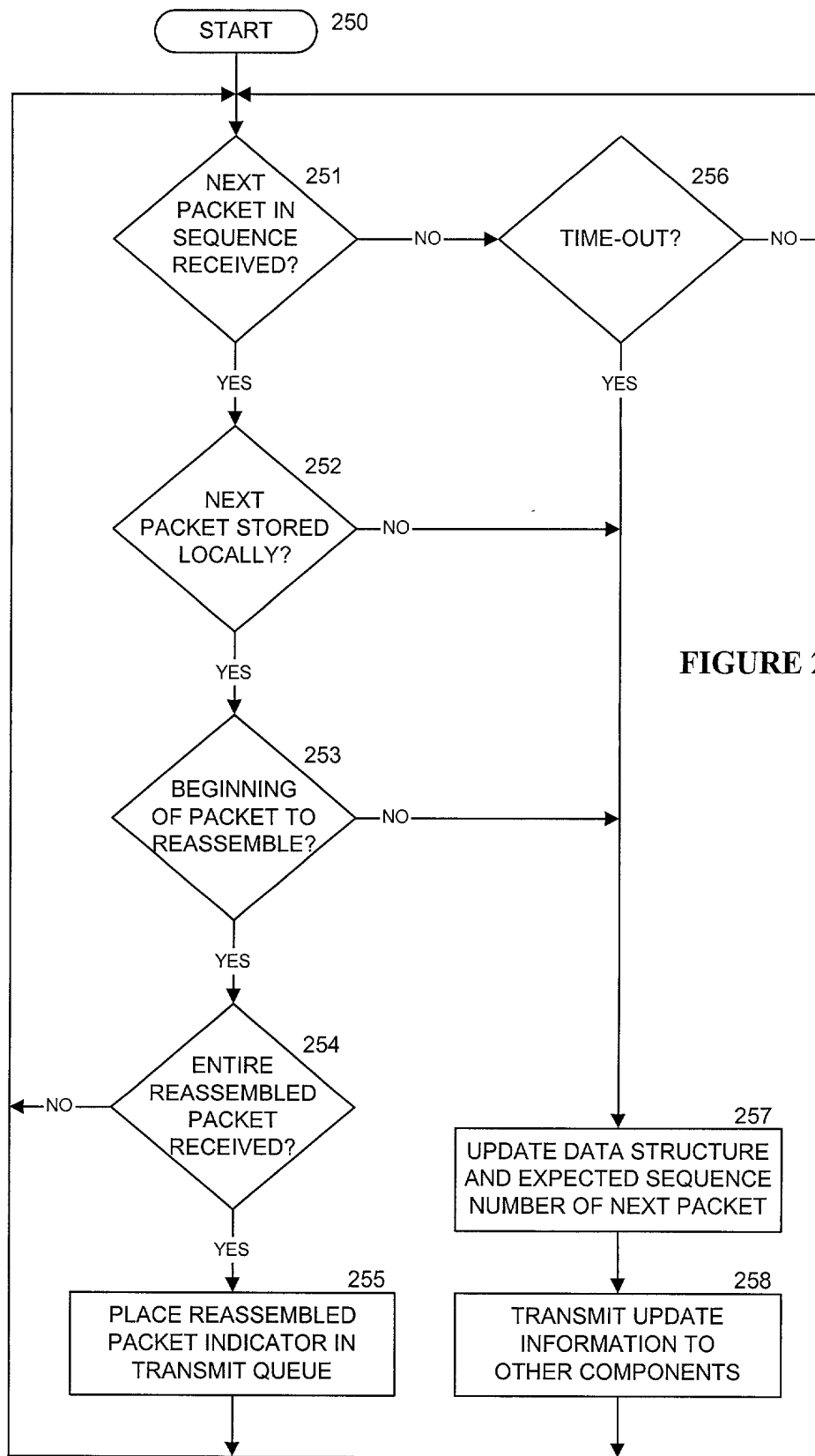


FIGURE 2F

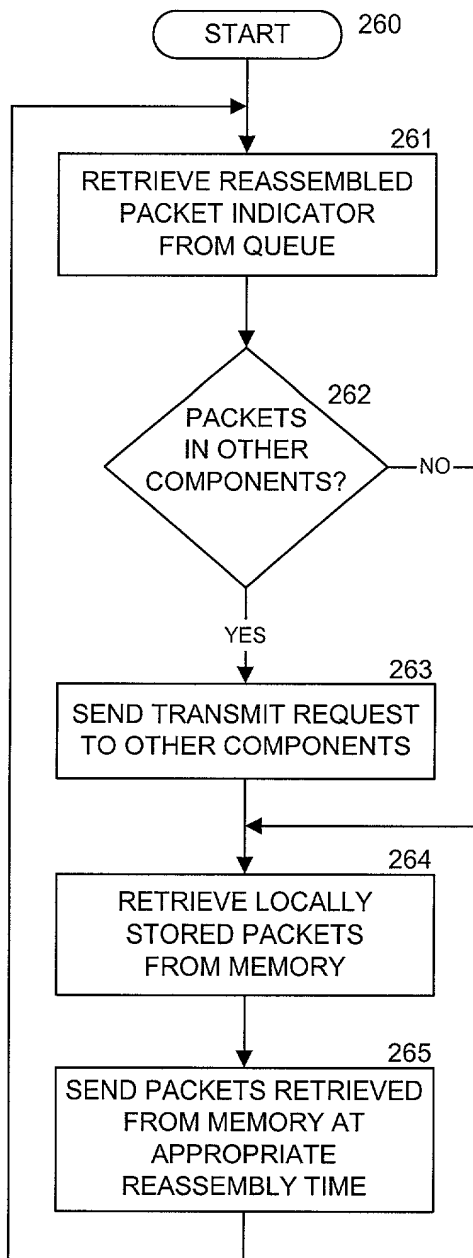


FIGURE 2G

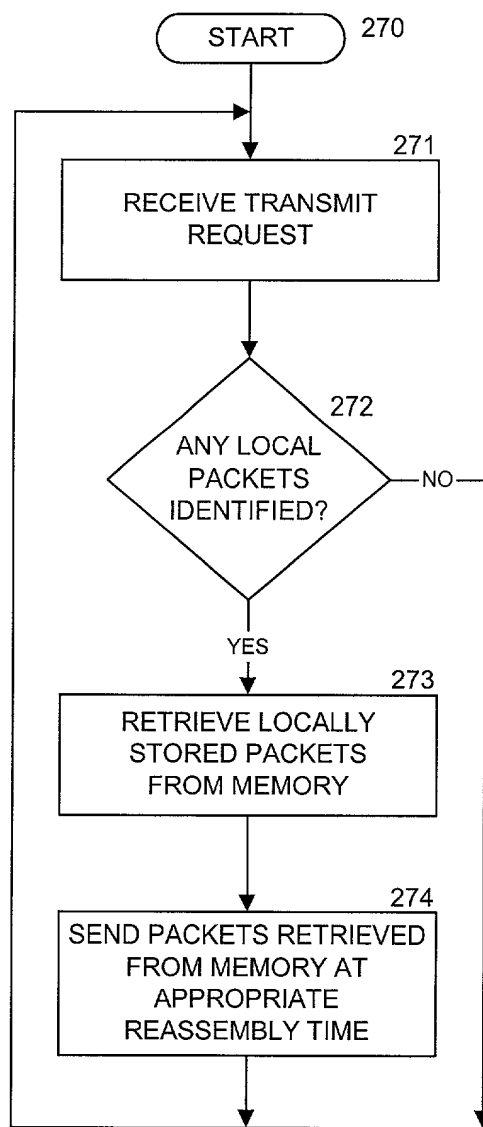


FIGURE 2H

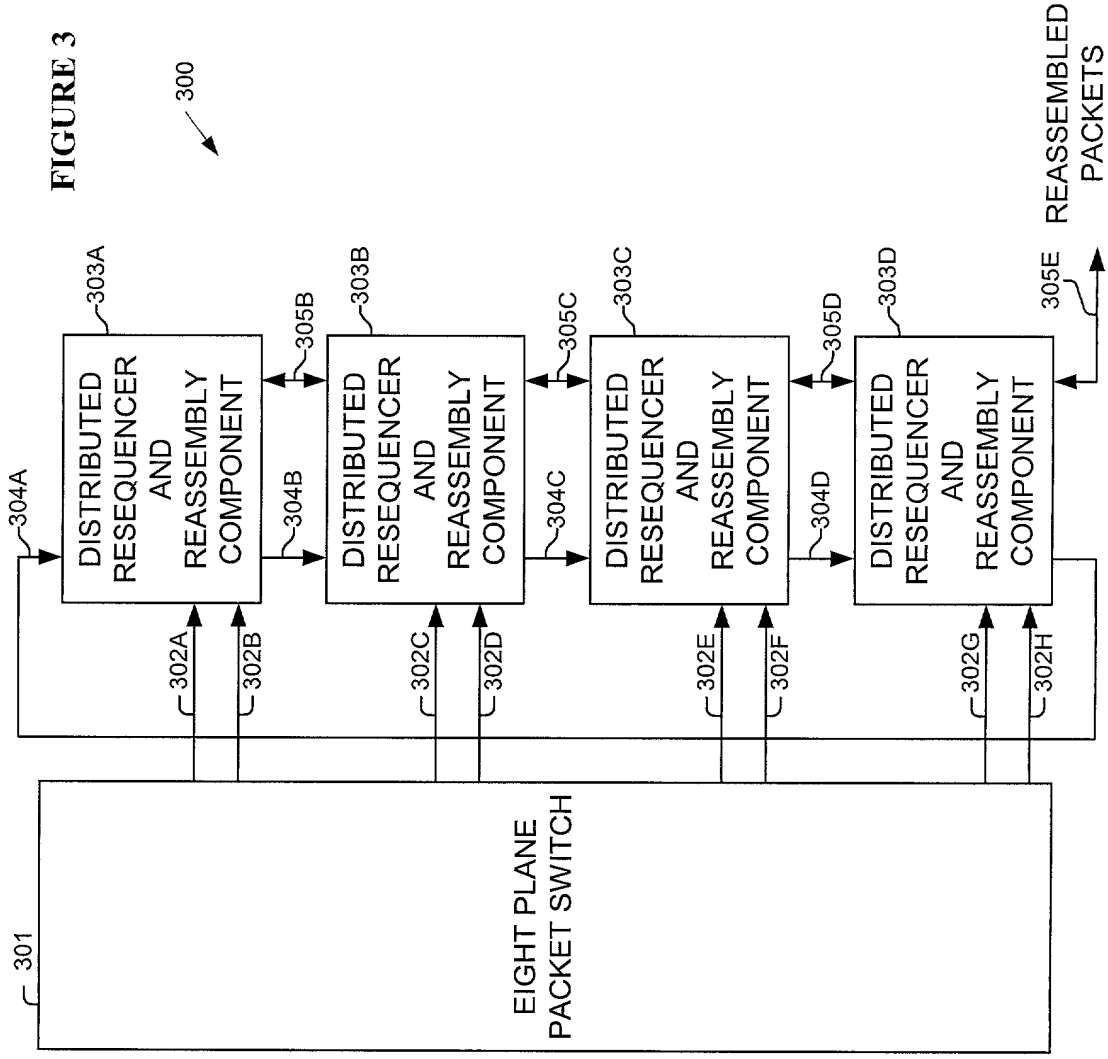


FIGURE 3

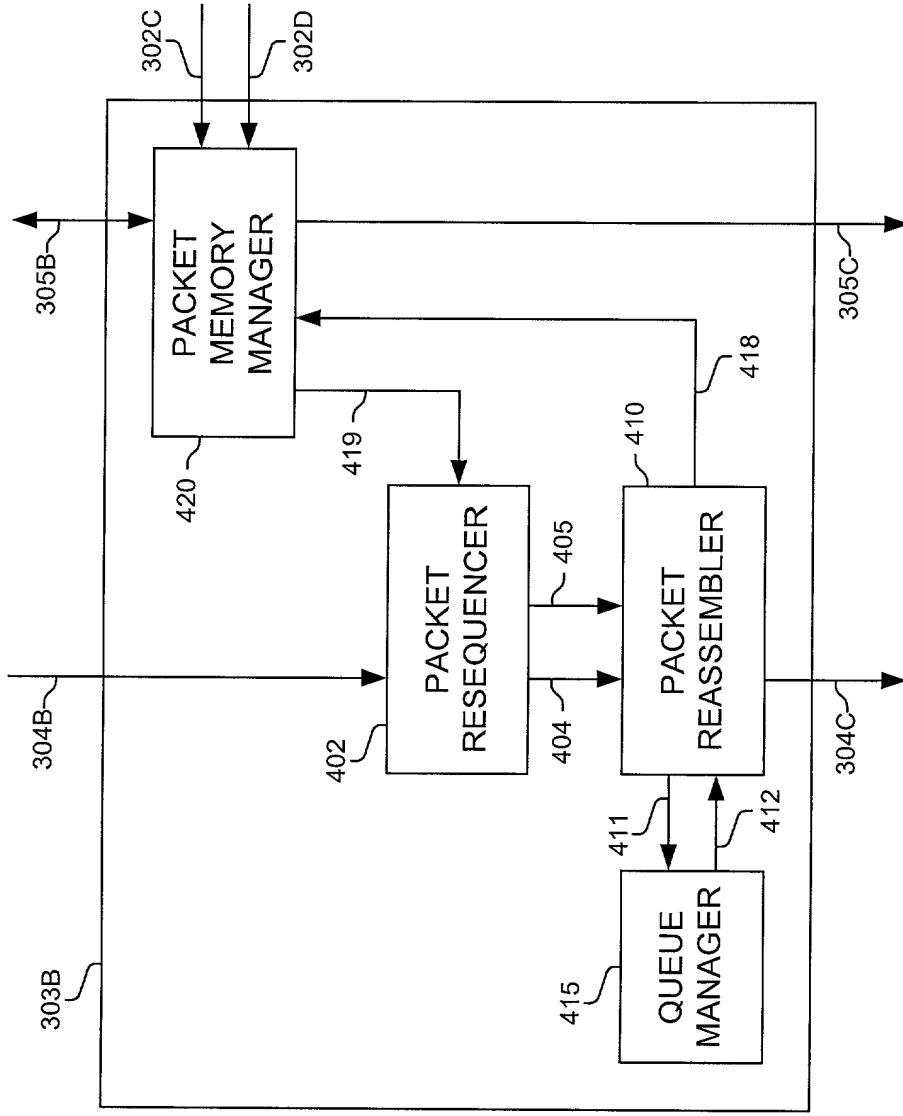


FIGURE 4A

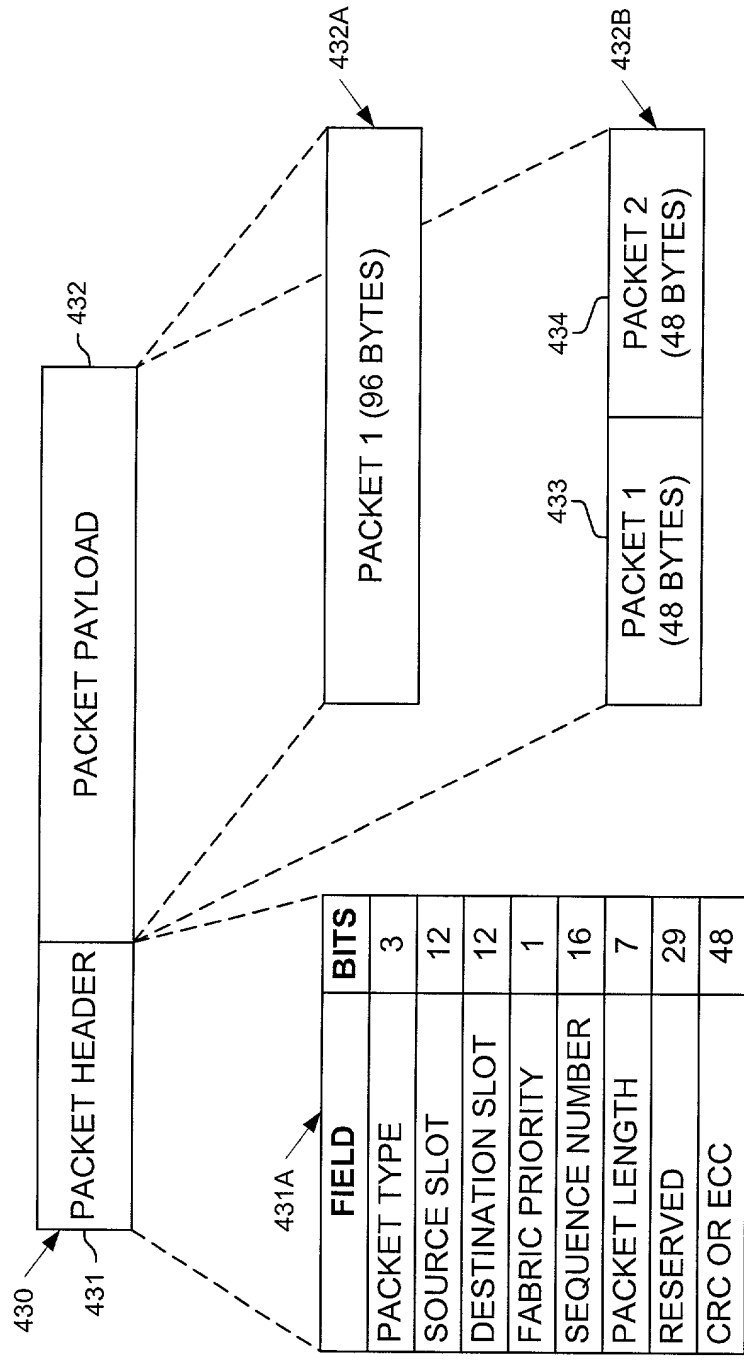
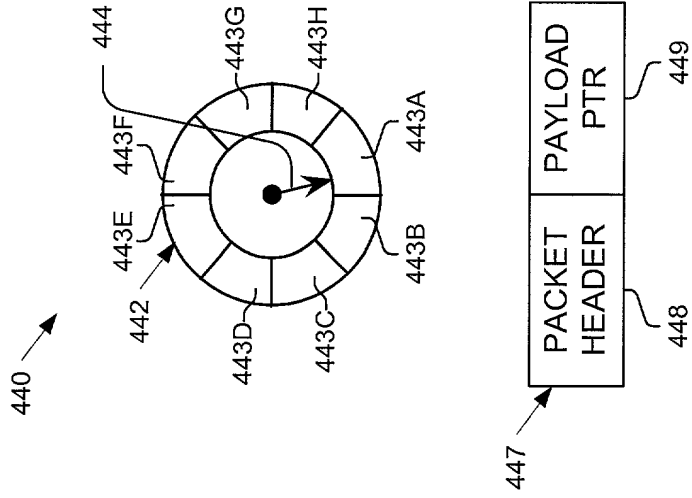
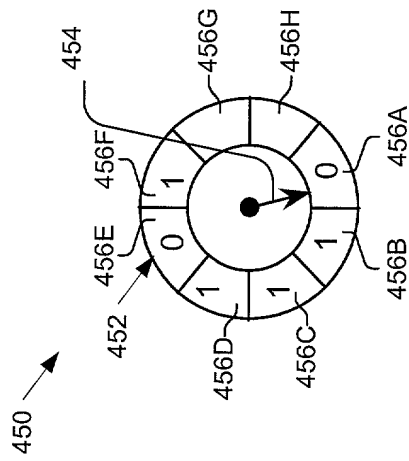


FIGURE 4B



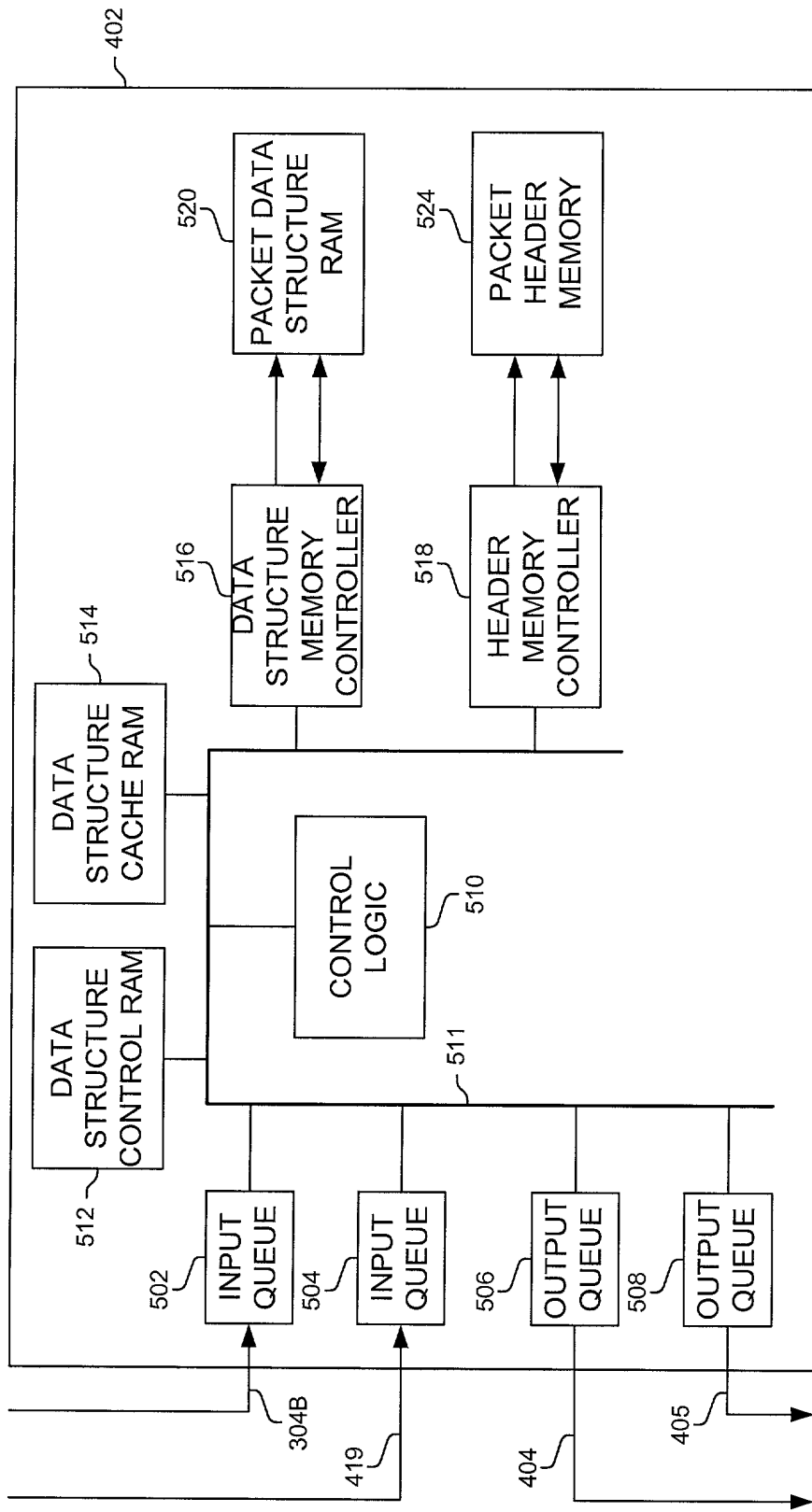
**LOCAL DATA
STRUCTURE**

FIGURE 4C

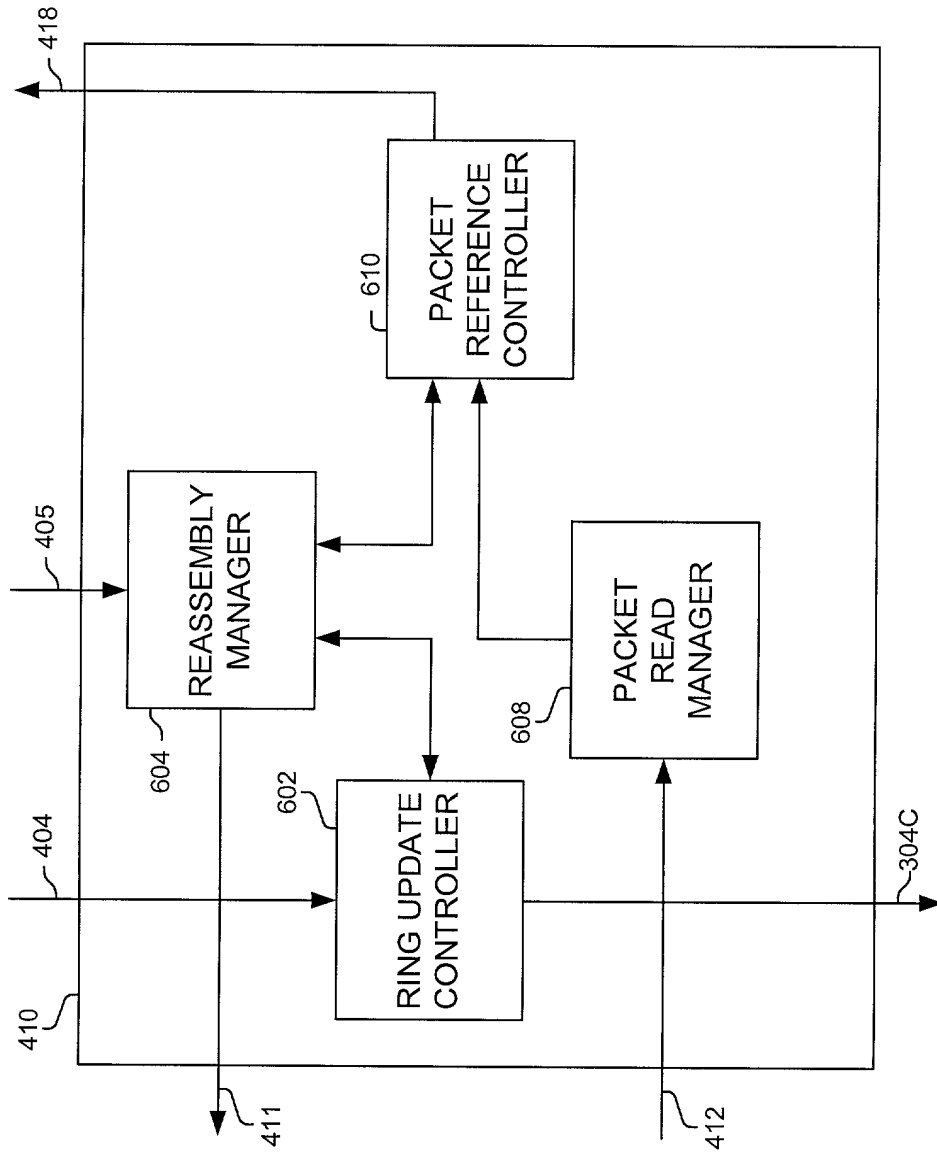


**GLOBAL DATA
STRUCTURE**

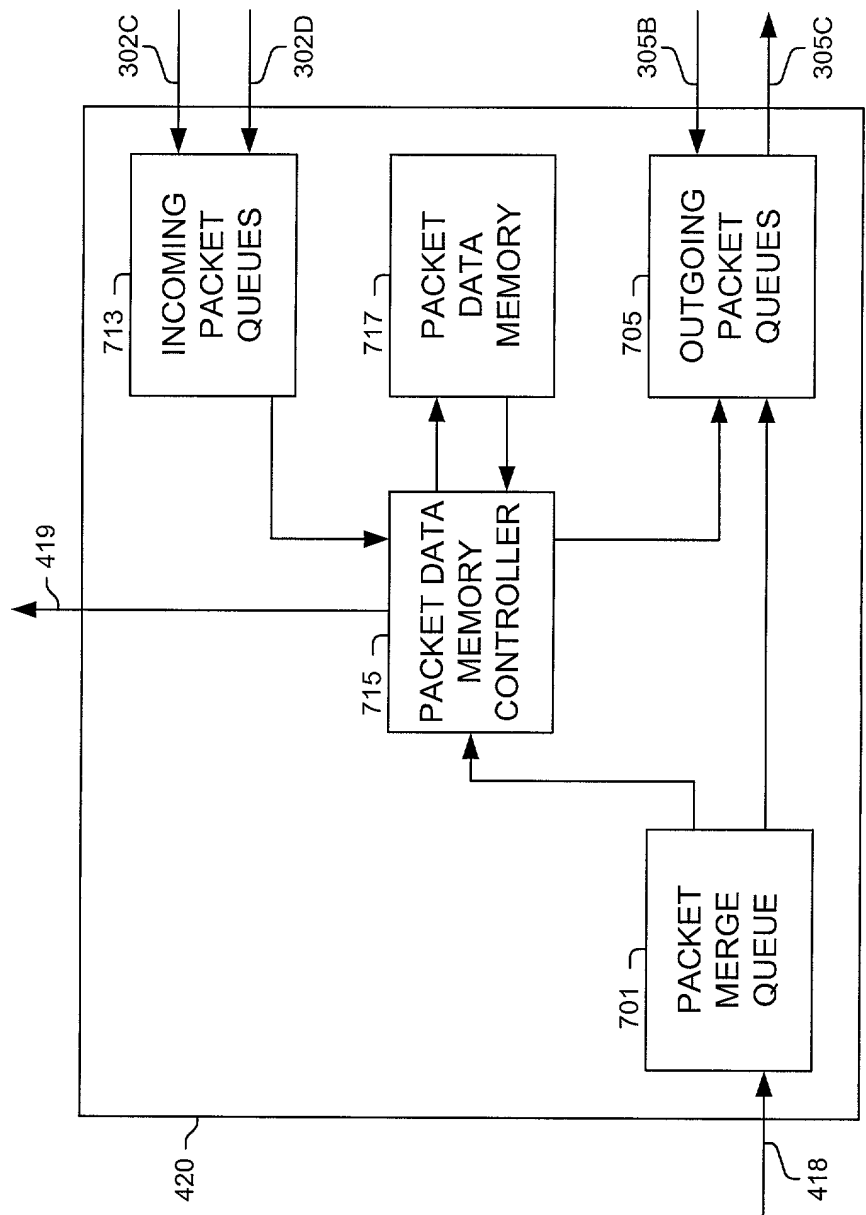
FIGURE 4D



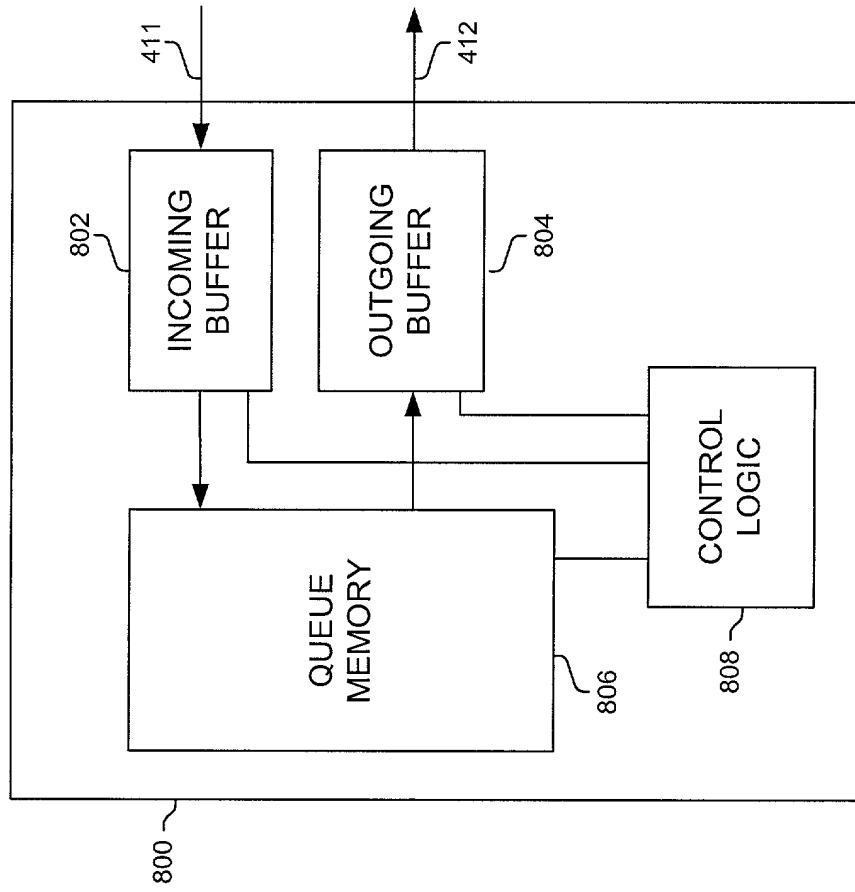
PACKET RESEQUENCER
FIGURE 5



PACKET REASSEMBLER
FIGURE 6



PACKET MEMORY MANAGER
FIGURE 7



QUEUE MANAGER
FIGURE 8